

Patent 7,743,223

PATENT

IN UNITED STATES PATENT AND TRADEMARK OFFICE

Patent No.: 7,743,223

Docket No: 1376.697US1

Issue Date: June 22, 2010

Patentee: Steven L. Scott et al.

Customer No.: 21186

Confirmation No.: 4130

Title DECOUPLING OF WRITE ADDRESS FROM ITS ASSOCIATED WRITE
DATA IN A STORE TO A SHARED MEMORY IN A MULTIPROCESSOR
SYSTEM

REQUEST FOR CERTIFICATE OF CORRECTION

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450
ATTN: CERTIFICATE OF CORRECTION BRANCH

It is requested that a Certificate of Correction be issued correcting printing errors appearing in the above-identified United States patent. A copy of the text of the Certificate in the suggested form is enclosed.

Authorization to charge Deposit Account No. 19-0743 in the amount of \$100.00 to cover the Certificate of Correction Fee.

Issuance of the Certificate of Correction would neither expand nor contract the scope of the claims as properly allowed, and re-examination is not required.

The Examiner is authorized to charge any additional fees or credit overpayment to Deposit Account No.19-0743.

Respectfully Submitted,

SCHWEGMAN, LUNDBERG & WOESSNER, P.A.
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Date: July 29, 2010

By: Thomas F. Brennan
Thomas F. Brennan
Reg. No: 35,075
TFB:rk

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is filed using the USPTO's electronic filing system EFS-Web, and is addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on this 30 day of July, 2010.

Kevin Austad
Name

Kevin Austad
Signature

UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO : 7,743,223

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DATED : June 22, 2010

INVENTOR(S) : Steven L. Scott et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

On the face page, in field (56), under "Other Publications", in column 2, line 1, delete "Tthe" and insert -- The --, therefor.

In column 2, line 52, delete "10/643,585" and insert -- 10/643,586 --, therefor.

In column 2, line 52, delete "'Decoupled Vector Architecture'," and insert -- "Decoupled Scalar/Vector Computer Architecture System and Method", --, therefor.

In column 2, line 64, delete "10/643,585," and insert -- 10/643,586, --, therefor.

In column 2, line 65, delete "'Decoupled Vector Architecture'," and insert -- "Decoupled Scalar/Vector Computer Architecture System and Method", --, therefor.

In column 5, line 20, delete "in" and insert -- is --, therefor.

In column 6, lines 29-38, delete "In one embodiment, global memory 26 as shown in FIG. 3 is distributed to each MSP 30 as local memory (not shown in FIGS.). In one embodiment, local memory is packaged as a separate chip (termed the "M" chip as shown in FIG. 4, block 26). Each Ecache 24 has four ports 34 to M chip 26 and connected through M chip 26 to local memory. In one embodiment, ports 34 are 16 data bits in each direction. MSP 30 has a total of 25.6 GB/s load bandwidth and 12.8-20.5 GB/s store bandwidth (depending upon stride) to local memory." and insert -- In one embodiment, global memory 26 as shown in FIG. 3 is distributed to each MSP 30 as local memory (not shown in FIGS.). In one embodiment, local memory is packaged as a separate chip (termed the "M" chip as shown in FIG. 4, block 26). Each Ecache 24 has four ports 34 to M chip 26 and connected through M chip 26 to local memory. In one embodiment, ports 34 are 16 data bits in each direction. MSP 30 has a total of 25.6 GB/s load bandwidth and 12.8-20.5 GB/s store bandwidth (depending upon stride) to local memory. --, below "stores." as a new paragraph.

In column 8, line 29, in Claim 1, delete "connected to" and insert -- connected --, therefor.

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Atty Docket No: 1376.697/US1

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UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

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DATED : June 22, 2010

INVENTOR(S) : Steven L. Scott et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In column 8, line 29, in Claim 1, after "network" insert -- to --.

In column 8, line 64, in Claim 4, delete "request" and insert -- requests --, therefor.

In column 9, line 1, in Claim 4, delete "in" and insert -- in the --, therefor.

In column 9, line 7, in Claim 5, delete "request" and insert -- requests --, therefor.

In column 9, line 12, in Claim 5, delete "in" and insert -- in the --, therefor.

In column 9, line 19, in Claim 6, delete "request" and insert -- requests --, therefor.

In column 9, line 31, in Claim 8, delete "request" and insert -- requests --, therefor.

In column 9, line 49, in Claim 10, after "the" insert -- first --.

In column 11, line 37, in Claim 24, delete "subsequent" and insert -- subsequent --, therefor.

In column 12, lines 45-46, in Claim 31, after "memory." delete "request is received and written by the shared memory."

In column 12, line 59, in Claim 32, after "the" insert -- write --.

In column 14, line 8, in Claim 35, after "the" insert -- write --.

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